1.   (Amended) A system for rendering graphics primitives using a shortened display list for ensuring a shortened processing time while maintaining the quality of the display information contained in the display list, the system comprising:

a system bus for communicating data and instructions;

a host processor coupled to the system bus for processing a display list defining a graphics primitive;

a system memory coupled to the system bus for storing the display list;

a graphics subsystem coupled to the system bus for processing display parameter values contained in the display list <u>including shortening display list instructions</u>, wherein the display list includes a field load instruction for effectively shortening display list processing; and

a display unit coupled to the graphics processor for displaying the graphics primitives comprising the display list.

10.   (Amended) A graphics system for processing parameter values of graphics primitives in a display list, [wherein the display list is shortened to enable fast processing time while maintaining the quality of information contained in the display list,] the graphics system comprising;

a plurality of register files for storing a plurality of parameter values representing graphics primitives defined in the display list; and

a graphics processor coupled to the plurality of register files, wherein the graphics processor <u>generates and</u> processes [the] <u>a</u> shortened display list <u>to enable faster processing of the graphic primitive</u>, while maintaining the display quality of primitives displayed in a display unit.

25. A method of [encoding and decoding] <u>processing</u> a shortened display list load instruction comprising the steps of:

encoding a field load instruction wherein the field load instruction comprises an opcode instruction, a write enable field and a partition index field;

loading the field load instruction to a register file; and executing the field load instruction in a shortened processing time.

<u>REMARKS</u>

Claims 1-30 are now present in this case. Consideration of the application in view of the above amendment and the following remarks is respectfully requested.

In the Office Action mailed August 20, 1998, the Examiner required restriction of prosecution to one of the following inventions: Group I including claims 1-9; Group II including claims 10-24; and Group III including claims 25-30.

Applicant hereby elects the invention of Group II including claims 10-24, drawn to graphics display list instruction processing with Traverse.

Applicant has amended claims 1, 8 & 25 and now submits that the restriction requirement is improper. Applicant has amended claim 1 to be directed to a system including a graphics subsystem capable of shortening display list instruction. Claim 8 has been amended to claim a graphics system that generates and processes a shortened display list. Finally, claim 25 has been amended to clarify that it is a method for processing a shortened display list load instruction. Therefore, Applicant submits that the groups I, II and III are related as a combination, subcombination and method; and that the combination and method now rely in part on the particulars of the subcombination for patentability.